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U.S. UTILITY Patent Application

APPL NUM 10069522	FILING DATE 05/29/2002	CLASS 324	SUBCLASS 523	GAU 2862	EXAMINER LA.2 A. LC
<b>**APPLICANTS:</b> Ishioka Shogo; Yamaoka Shuji; 285E					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A 371 OF PCT/JP01/04994 06/13/2001 28					
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<b>** FOREIGN APPLICATIONS VERIFIED:</b> 28 JAPAN 2000-182118 06/16/2000					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials		Donald M. Fair		020239	
TITLE : Device and method for inspecting circuit board					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner  Primary Examiner	
<b>ISSUE FEE</b> Amount Due      Date Paid		Total Claims      Print Claim for O.G.  <b>DRAWING</b> Sheets Drawg.      Figs. Drawg.      Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner  <b>PREPARED FOR ISSUE</b>	
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